

Fig. 1

2

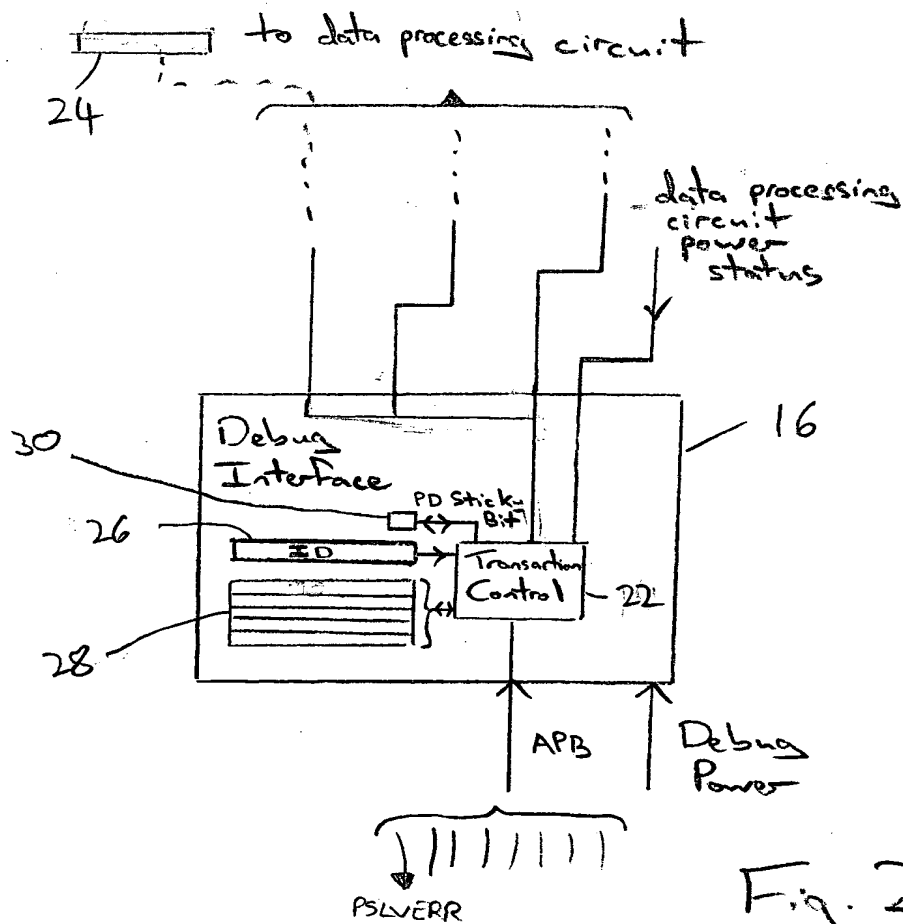
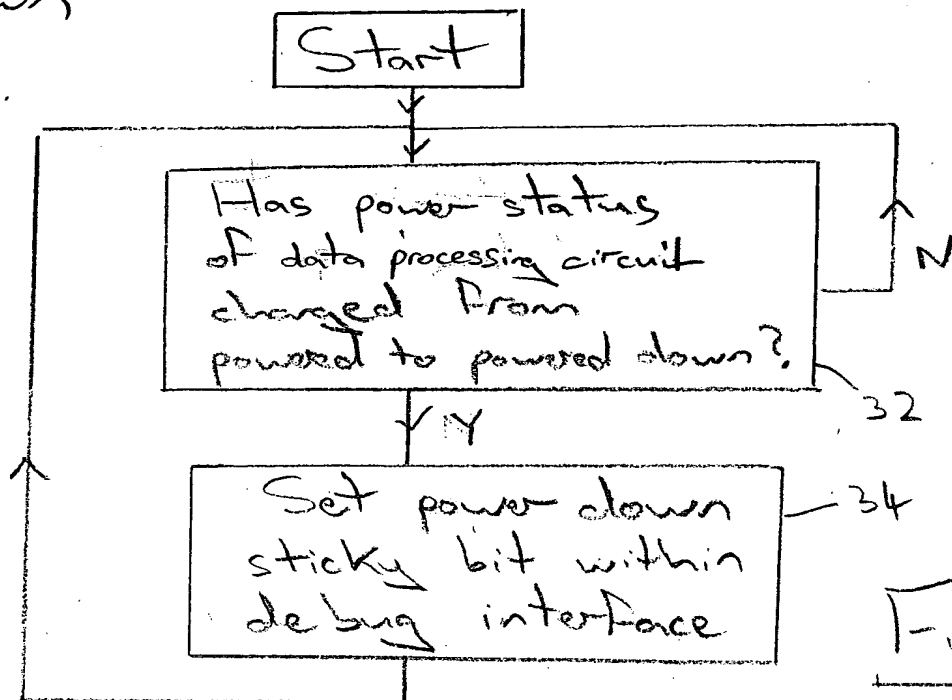


Fig. 2

Power Down
Sticky
Bit
Setting



other edge trigger part.

Fig. 3

Debug Bus Error Generation

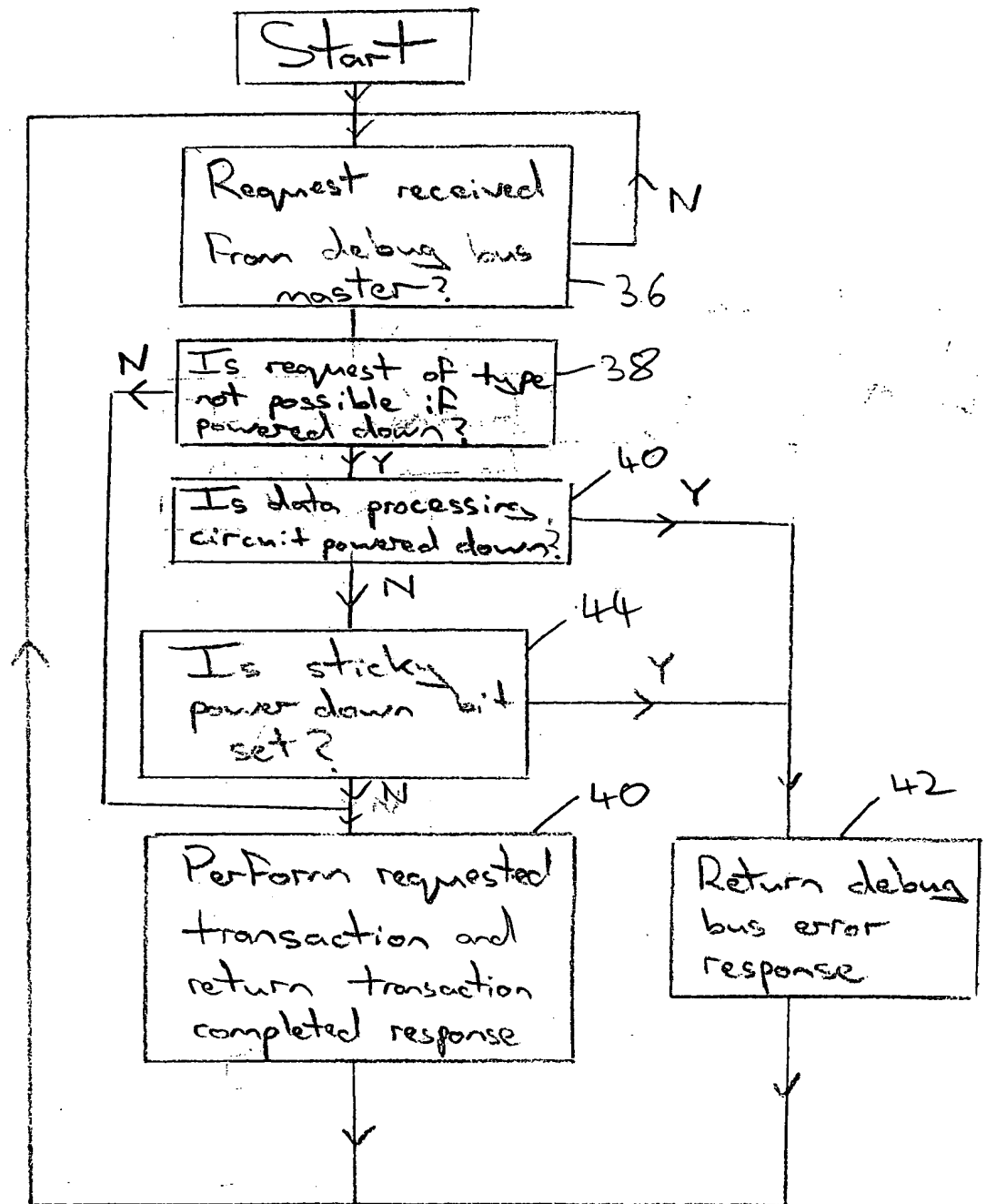


Fig. 4